

10033694  
12/28/2001  
438  
107  
2812

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10033694	12/28/2001	438	107	2812	JEH/MLH

\*\*APPLICANTS: Salvatore Leonardi;

\*\*CONTINUING DATA VERIFIED:

NONE

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\*\* FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 00830870.2 12/29/2000.

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no

35 USC 119 conditions met ☒ yes ☐ no

Verified and Acknowledged Examiners's initials *JEH*

ATTORNEY DOCKET NO

856063.721

TITLE : Integration process on a SOI substrate of a semiconductor device comprising at least a dielectrically isolated well

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for 0.6
		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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